## **Patent**

IFW

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of:

Nick Lindert

Examiner:

Nguyen, Joseph H.

U.S. Serial No:

10/816,311

Art Unit:

2815

Filed:

March 31, 2004

For: A BULK NON-PLANAR TRANSISTOR HAVING STRAINED ENHANCED MOBILITY AND METHODS OF FABRICATION

Commissioner of Patents and Trademarks P.O. Box 1405 Alexandria, VA 22313-1450 I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450

Date of Deposit

Name of Person Mailing Correspondence

ignature Date

## **RESPONSE TO ELECTION REQUIREMENT**

Sir:

In response to the Election Requirement mailed March 15, 2005, applicant(s) hereby elects to prosecute Group I claims 1-19, drawn to a semiconductor device. As such, applicant(s) elects to withdraw Group II claims 20-42, drawn to a method of making a semiconductor substrate.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN

Date:  $\frac{3}{22}$ , 2005

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